## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		)	Group Art Unit: 2183	
	Takashi Mita et al.	)	Evaminer	Fennema, Robert E.
Serial No.:	10/500,197	)	Examiner.	remena, Robert E.
Filed:	June 24, 2004	)		
For:	LOGIC COMPUTING SYSTEM AND RELATED METHOD HAVING PRE-LOADED ON-CHIP CONFIGURATION DATA	) ) ) )		

August 9, 2007

## Submitted via EFS

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

## AMENDMENT UNDER 37 C.F.R. 1.116

This communication is in response to the Final Office Action of April 10, 2007. The three-month response deadline expired on July 10, 2007. Thus, this Amendment is being submitted with the fee for a one-month Extension of Time to extend the response period to August 10, 2007. Accordingly, this Amendment is timely. In addition, this Amendment is being filed with a Request for Continued Examination, along with the required fee, so that the amendments and arguments submitted herein will be considered.